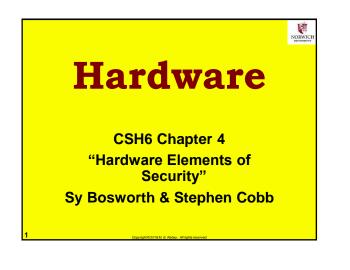
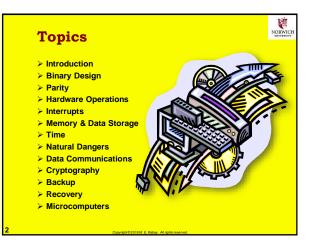
# Introduction to IA – Class Notes





### Introduction

Other hardware-related chapters of CSH6:

- Ch 5 Data Communications & Information Security
- > Ch 6 Network Topologies, Protocols, & Design
- Ch 22 Physical Threats to the Information Infrastructure
- Ch 23 Protecting the Information Infrastructure

# Distance of the provided with the provi

### Circuitry

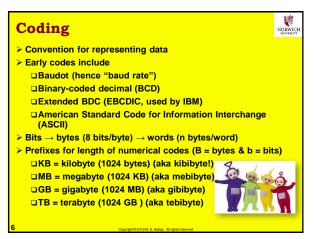
- Original 1940s computers used vacuum tubes
  - Relatively large room-sized machines with power of later calculator-wristwatches
     Consumed power & ran hot



唐

NORWIC

- □Short MTBF (mean time between failures)
- Solid-state transistors
  - □Patented in 1925 (Lilienfeld) & 1934 (Heil) but never developed
  - William Shockley & colleagues at Bell Labs developed effective transistors starting in 1947



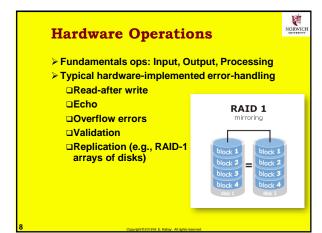
NORWICH

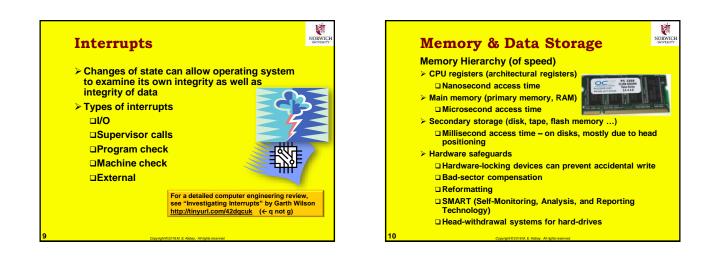
### **Error-Detecting Codes**

- Error-detection systems started with parity bits
- > Write additional bit into hardware storage (e.g., disk, RAM, data-comm, tape...)
  - □Even parity bit = 0 when sum of bits is even
  - □Odd parity bit = 0 when sum of bits is odd
  - Recompute parity bit when reading data back
    Check to see if computed parity bit = recorded
  - parity bit
- Extensions led to error-correcting codes;
  - e.g.,
  - Cyclical Redundancy Checks (CRCs)

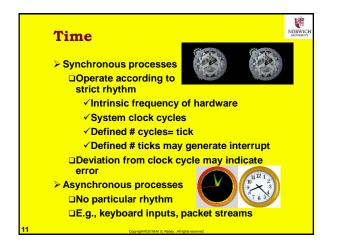
□Self-checking codes

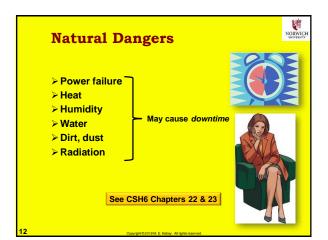






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